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J Wolkerstorfer, E Oswald, M Lamberger - Proc. RSA Conference, 2002 - Springer ... provides the mathematical background of the finite **field** arithmetic and the computation of the **AES** SBoxes in Sect. 2. The building blocks of an **SBox** and the ...

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S Morioka, A Satoh - Very Large Scale Integration (VLSI) Systems, IEEE ..., 2004 - ieeexplore.ieee.org

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S Morioka, A Satoh - Proc. CHES, 2002 - Springer

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E Oswald, S Mangard, N Pramstaller, V Rijmen - Fast Software Encryption, 12th International Workshop, FSE, 2005 - Springer

... Our approach is based on shifting the computation of the finite **field** inversion in the **AES S-box** down to GF(4). In this **field**, the inversion is a linear ...

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CC Lu, SY Tseng - Application-Specific Systems, Architectures and Processors, ..., 2002 - ieeexplore.ieee.org

... the matrix M. By the finite field inverse matrix ... SubBytes/InvSubBytes module (inverse-optional S-box module), capable ... use in encryption and decryption of AES .... Cited by 37 - Related Articles - Web Search

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F Rodriguez-Henriquez, NA Saqib, A Diaz-Perez - Electronics Letters, 2003 ieeexplore.ieee.org

... The AES S-box is a 256-entry table composed of ... while IAF +MI computes the Inverse S-box needed for ... where xtime(v) represents the finite field multiplication of ... Cited by 19 - Related Articles - Web Search - BL Direct

Minimum area cost for a 30 to 70 Gbits/s AES processor - all 9 versions » A Hodjat, I Verbauwhede - VLSI, 2004. Proceedings. IEEE Computer society Annual ..., 2004 - ieeexplore.ieee.org

... using the pipelined implementation of the composite Galois Field ... design of the composite

field implementation of ... to 100 Gbits/s Throughput AES Processor', 37 ... Cited by 23 - Related Articles - Web Search

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J Blomer, JG Merchan, V Krummel - Selected Areas in Cryptography, 11th International Workshop, ..., 2004 - Springer

... using arithmetic operations defined over some finite field into a ... k that is co-prime to p. The field F ... Combinational Logic Design for AES S-Box on Masked Data. ... Cited by 27 - Related Articles - Web Search

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